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2822

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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MASSUD AMINPUR  
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§

Examiner: Khanh B. Duong  
Group Art Unit: 2822

Serial No.: 09/676,197

§  
Att'y Docket: 2000.032200/TT3634

Filed: September 28, 2000

§

For: METHOD OF FORMING A GATE  
ELECTRODE ON A  
SEMICONDUCTOR DEVICE AND A  
DEVICE INCORPORATING SAME

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§  
§  
§

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1C 2800 MAIL ROOM

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**RESPONSE TO RESTRICTION  
REQUIREMENT DATED JUNE 4, 2002**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Assistant Commissioner for Patents, Washington, DC 20231, this 18<sup>th</sup> day of June, 2002.

  
\_\_\_\_\_  
Signature

This paper is submitted in response to the Restriction Requirement dated June 4, 2002, for which the one-month date for response is July 4, 2002.

In response to the restriction requirement which the Examiner imposed, Applicants elect, with traverse, to prosecute claims 1-20, *i.e.*, the Group I claims.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Assistant Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3634. In the event the monies in that account are insufficient, the Assistant Commissioner is authorized to

withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.032200.

The Examiner is invited to contact the undersigned attorney at (713) 934-4055 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,



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